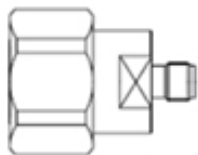


AD00618-7/1

PIMD



4.3/10 DIN (P)

SMA (J)

ST L:30.5mm

BO/SU, PI/SI/3, CAP/NI

AD00618-7/1

|                |               |
|----------------|---------------|
| Frequency      |               |
| Insertion Loss |               |
| VSWR           |               |
| Impedance      | ■ 50Ω □ 75Ω   |
| Temperature    | -40°C ~ 125°C |

S11/S22/S21(SWR, SWR, dB)

|                              |
|------------------------------|
| ■ _1.0GHz - 1.03, 1.04, 0.05 |
| ■ _2.0GHz - 1.03, 1.03, 0.02 |
| ■ _3.0GHz - 1.12, 1.12, 0.00 |
| ■ _4.0GHz - 1.15, 1.16, 0.04 |
| ■ _5.0GHz - 1.05, 1.07, 0.06 |
| ■ _6.0GHz - 1.12, 1.16, 0.10 |
| -----                        |
| ■ _7.0GHz - 1.25, 1.23, 0.18 |
| ■ _8.0GHz - 1.48, 1.20, 0.87 |
| MAX 8.1G - 2.41, 1.99, 3.61  |

► NETWORK ANALYZER

► Test cable

► Test Adaptor

■ E5071B (~8.5GHz)

■ N Male to N Male

■ 4.3/10 (J) N (J) AD00582-7/1

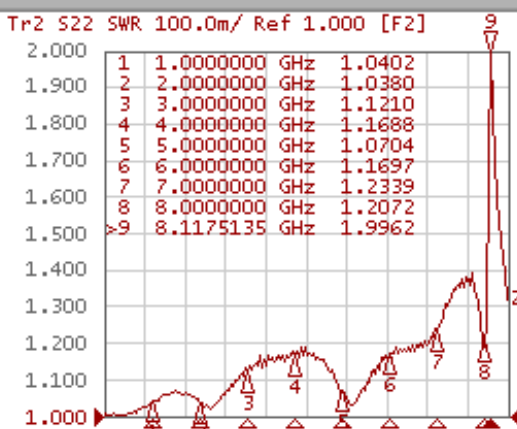
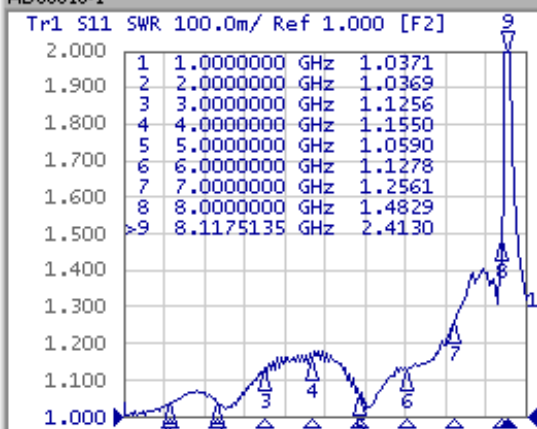
□ ZVA50 (~50GHz)

10GHz Cable (CF00539-7/1)

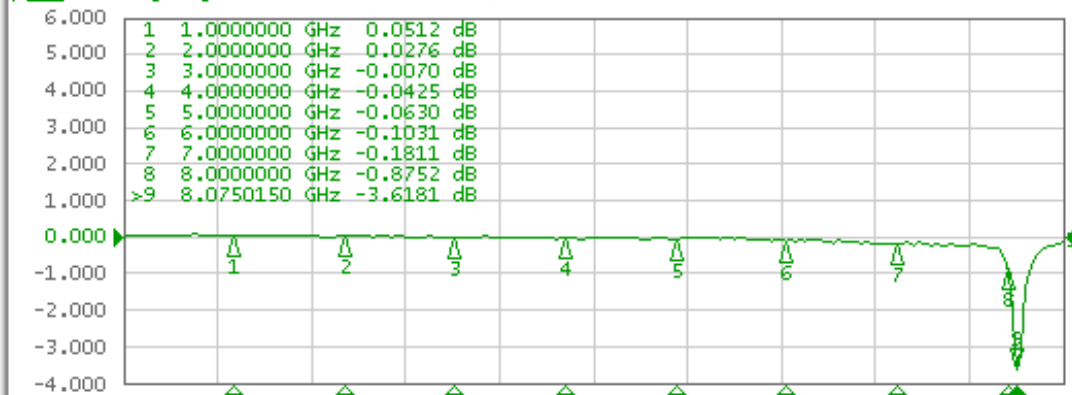
■ N (J) SMA (P) AD00423-7/1

1 Active Ch/Trace 2 Response 3 Stimulus 4 Mkr/Analysis 5 Instr State

AD00618-1



Tr3 S21 Log Mag 1.000dB/ Ref 0.000dB [F2]



1 Start 300 kHz

IFBW 70 kHz

Stop 8.5 GHz

Meas Stop ExtRef Ready Svc 2019-11-06 16:33

Display

Allocate Channels

Num of Traces  
3

Allocate Traces

Display Data

Data -> Mem

Data Math  
OFF

Equation Editor...

Equation  
OFF

Edit Title Label

TR1: IN PORT VSWR(S11), TR2: OUT PORT VSWR(S22), TR3: Insertion Loss (S21)