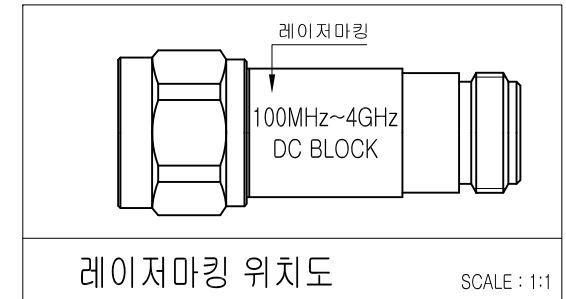
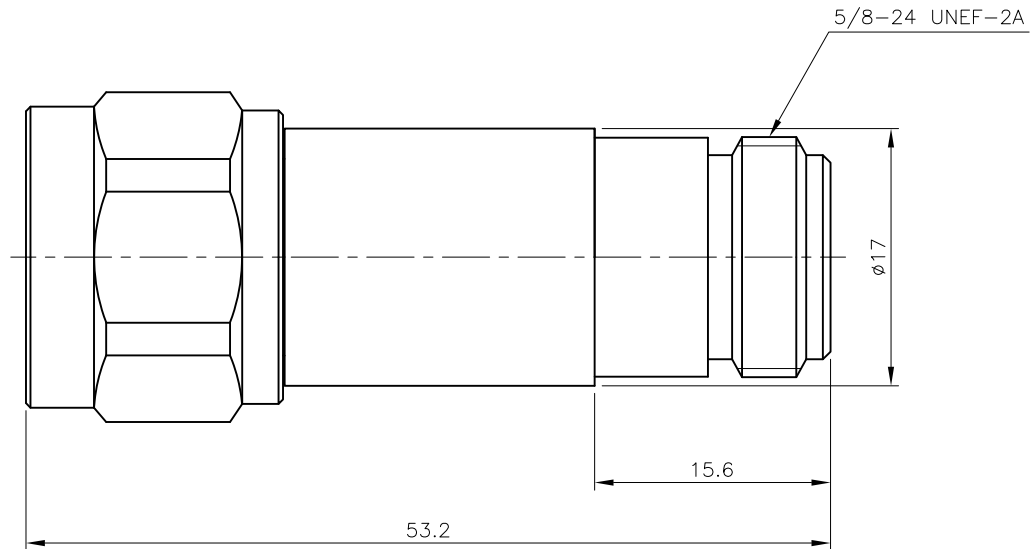


REVISION				
REV	DESCRIPTION	DRAWN	APPR	DATE
IR	Initial Registration		Y.H.CHOI	2010.02.17.
1	[설변처리No.201201-0008] 특성개선 PCB 뒀면 수정	I.C.KIM	J.H.YOUN	2012.01.18.
2	특성개선 Rev.	K.S.KIM	S.L.SON	2023.03.21.



☒	☐	☐
inner	outer	inner/outer

ELECTRICAL SPECIFICATIONS

1	IMPEDANCE	50Ω NOMINAL
2	FREQUENCY RANGE	100MHz ~ 4GHz
3	V.S.W.R	1.25 : 1 (Max)
4	Insertion Loss	1.0dB (Max)
5	Voltage Rating	50 V

TOLERANCE	Decimal ±0.1	DATE 2023. 03. 21.			 RF & MICROWAVE TECHNOLOGY Tel : 82-32-347-8015 Fax : 82-32-347-8017 KJ COMTECH CO.,LTD.			
	Angle ±1°	APPROVED BY	S.L.SON					
MATERIAL		CHECKED BY	S.L.SON		TITLE N50-A-JP DC/BLOCK(방수)			
		DRAWN BY	K.S.KIM					
FINISH		SCALE 2/1	UNIT mm		A4	DWG NO.	DB00001-7/2	
						REVISION	2	SHEET