

AD00354-7/1



7/16 DIN (J) SMA (J) 4 HOLE

□40.3-29.8-4.5

L:39.1mm(15-5.1-19)

BO/NI, PI/SI

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|                |               |
|----------------|---------------|
| Frequency      |               |
| Insertion Loss |               |
| VSWR           |               |
| Impedance      | ■ 50Ω □ 75Ω   |
| Temperature    | -40°C ~ 125°C |

S11/S22/S21(SWR, SWR, dB)

|           |                    |
|-----------|--------------------|
| ■ _1.0GHz | - 1.03, 1.03, 0.03 |
| ■ _2.0GHz | - 1.03, 1.03, 0.07 |
| ■ _3.0GHz | - 1.18, 1.20, 0.11 |
| ■ _4.0GHz | - 1.09, 1.09, 0.11 |
| -----     |                    |
| ■ _5.0GHz | - 1.38, 1.40, 0.20 |
| ■ _6.0GHz | - 1.31, 1.26, 0.20 |
| ■ _7.0GHz | - 1.43, 1.46, 0.21 |
| ■ _8.0GHz | - 1.20, 1.20, 0.12 |

► NETWORK ANALYZER

► Test cable

► Test Adaptor

■ E5071B (~8.5GHz)

■ N Male to N Male

■ 7/16 (P) N (J) AD00377-7/4

□ ZVA50 (~50GHz)

10GHz Cable (CF00539-7/1)

■ N (J) SMA (P) AD00423-7/1

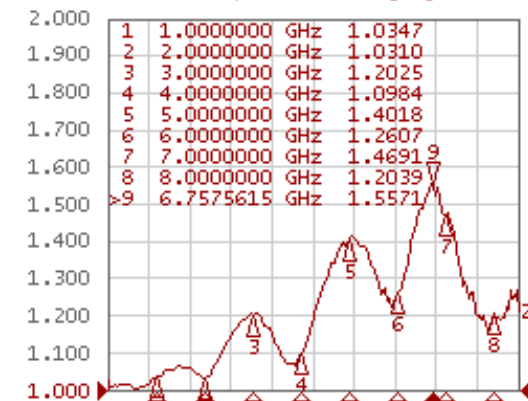
1 Active Ch/Trace 2 Response 3 Stimulus 4 Mkr/Analysis 5 Instr State

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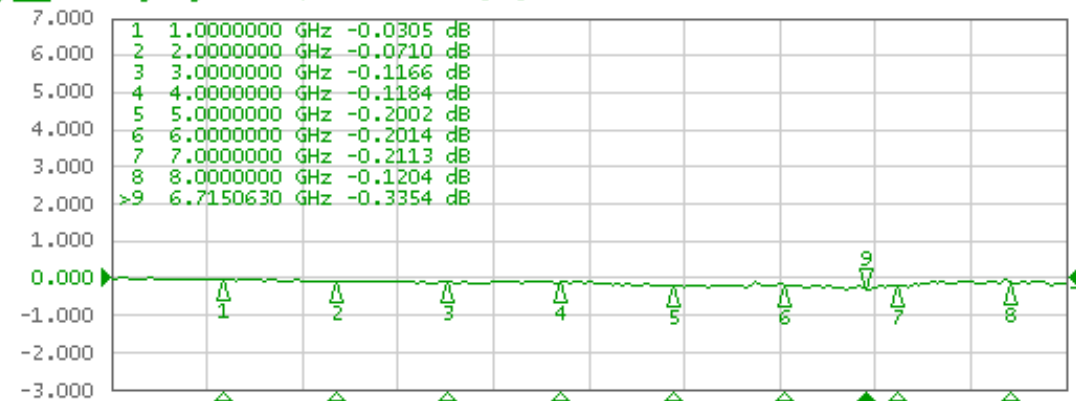
Tr1 S11 SWR 100.0m/ Ref 1.000 [F2]



Tr2 S22 SWR 100.0m/ Ref 1.000 [F2]



Tr3 S21 Log Mag 1.000dB/ Ref 0.000dB [F2]



1 Start 300 kHz

IFBW 70 kHz

Stop 8.5 GHz

Meas Stop ExtRef Ready Svc

2019-10-30 12:09

Display

Allocate Channels

Num of Traces  
3

Allocate Traces

Display Data

Data -> Mem

Data Math  
OFF

Equation Editor...

Equation  
OFF

Edit Title Label

TR1: IN PORT VSWR(S11), TR2: OUT PORT VSWR(S22), TR3: Insertion Loss (S21)